

VG2160

Configuration
Drawing

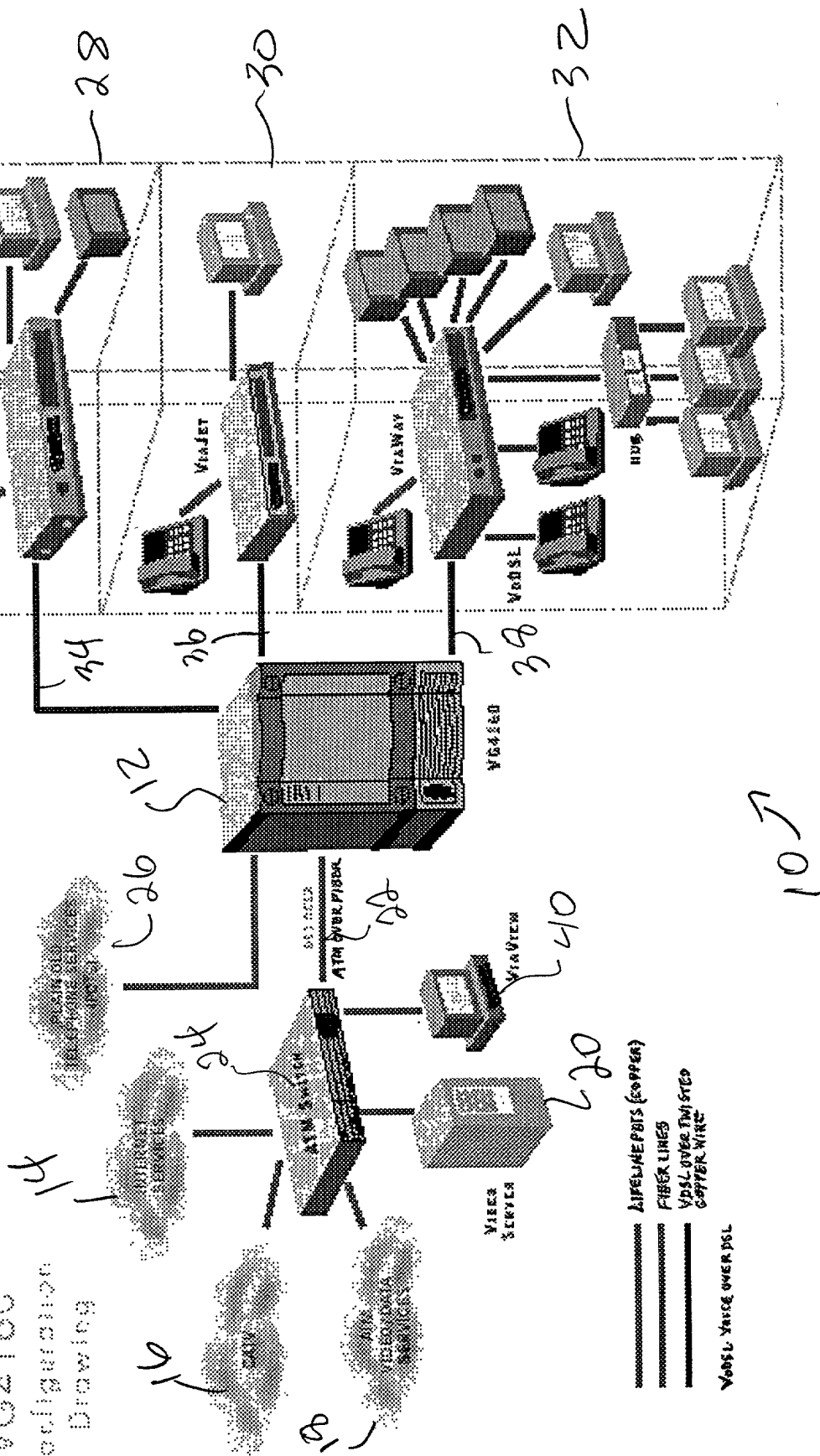
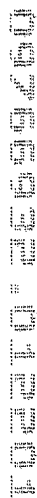
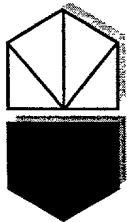


Fig. 1



VG 4000⁺ ARCHITECTURE

Fig. 2



CSM Block Diagram

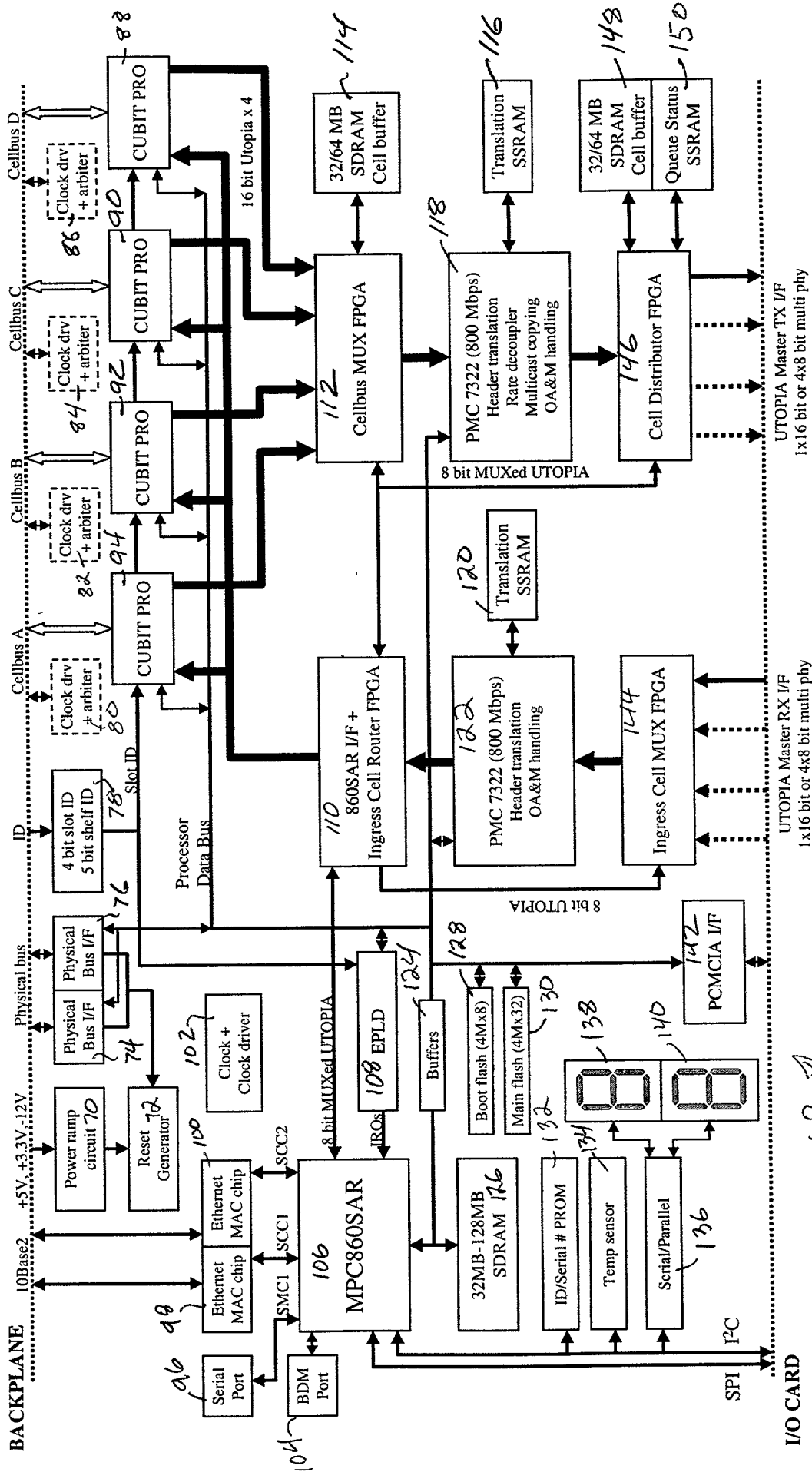


Fig. 3

64,66,68 A



System Controller I/O Card

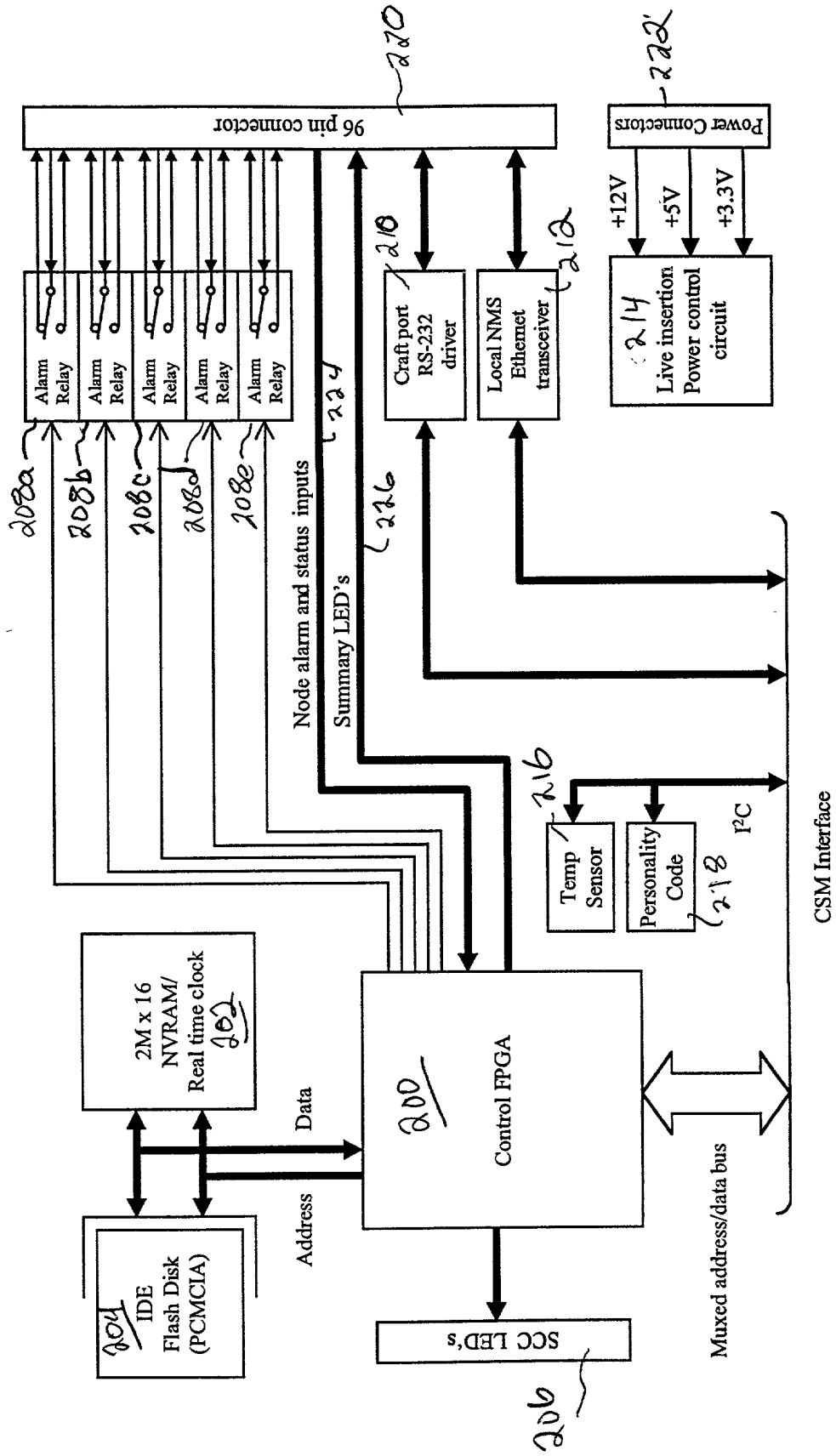
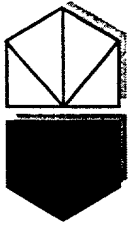


Fig. 4



Quad OC3 Block Diagram

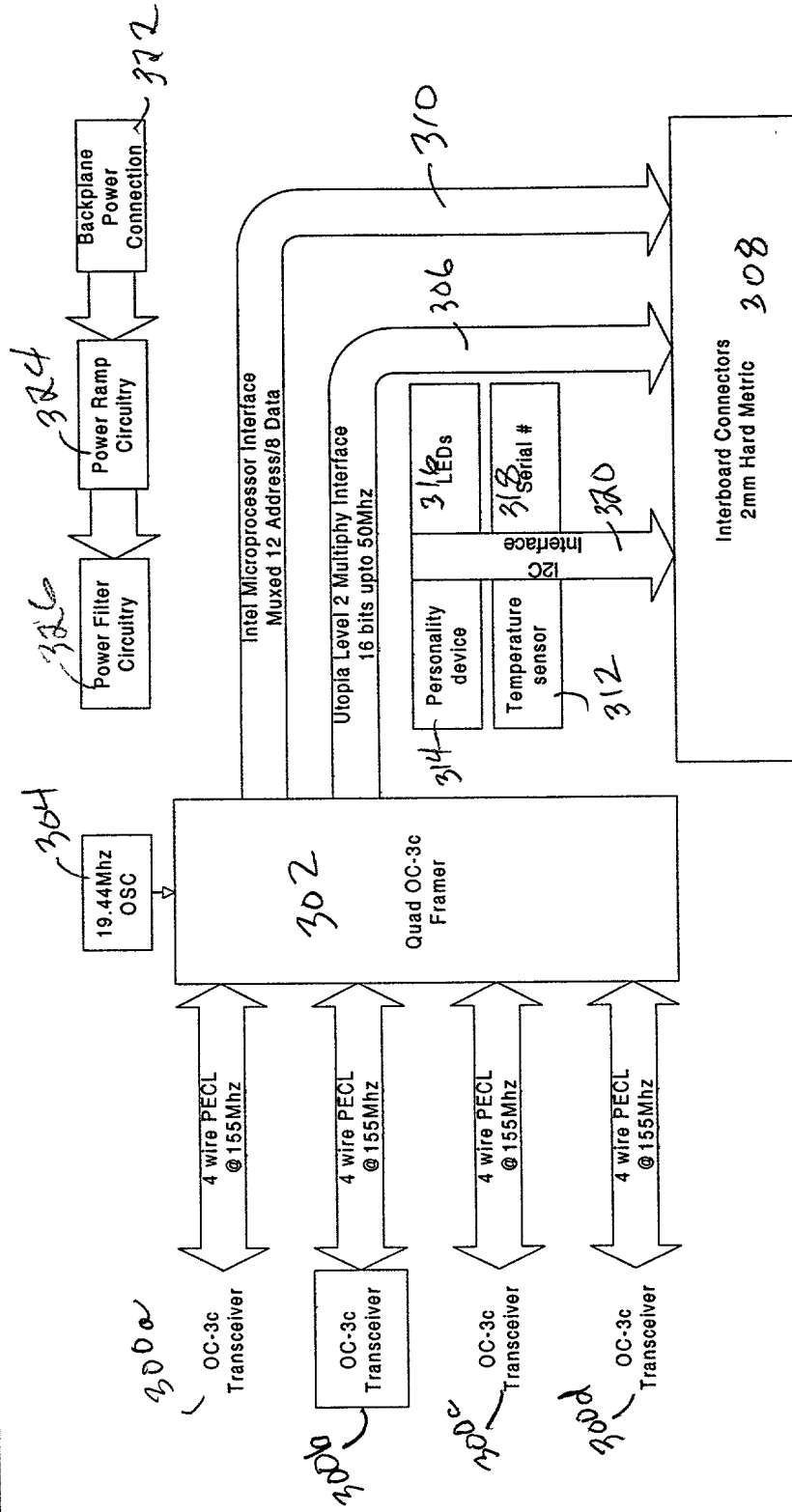
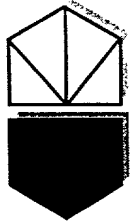
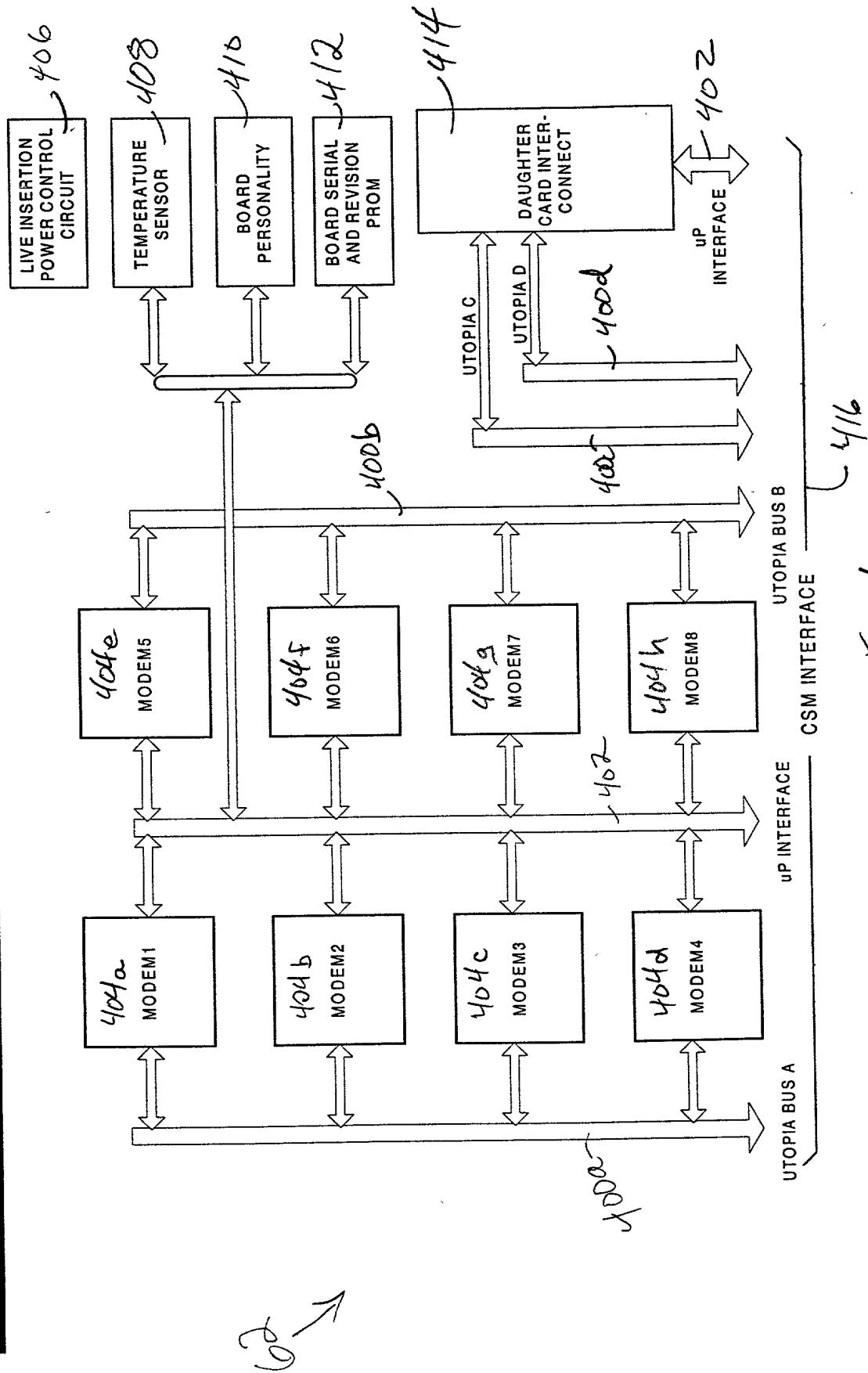


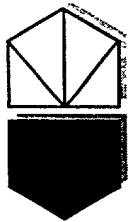
Fig. 5

60 →



VLC - VDSL Line Card





ViaJet A40x Block Diagram

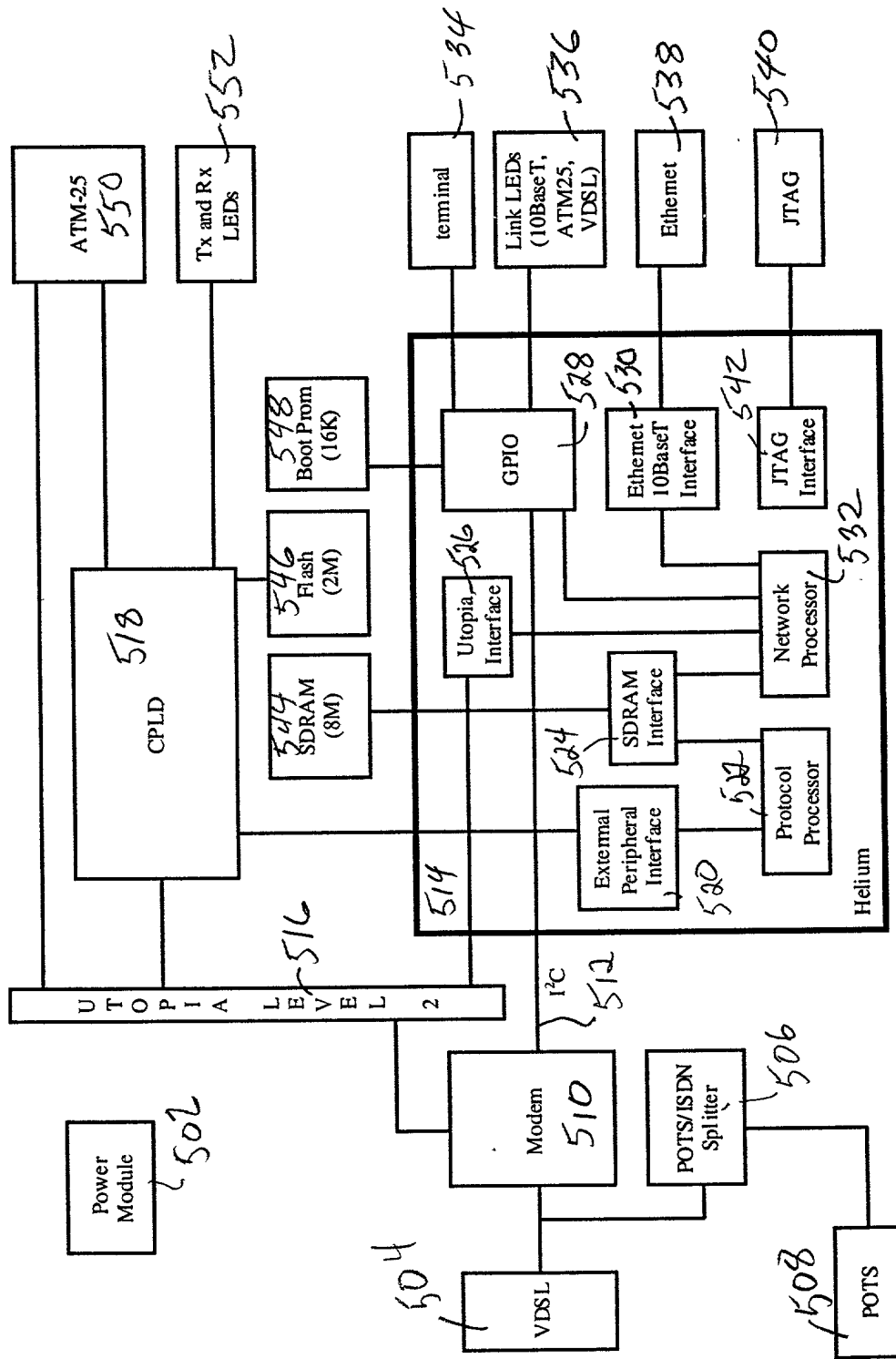
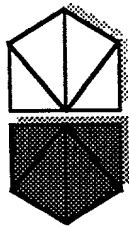


Fig. 7a



ViaWay A406

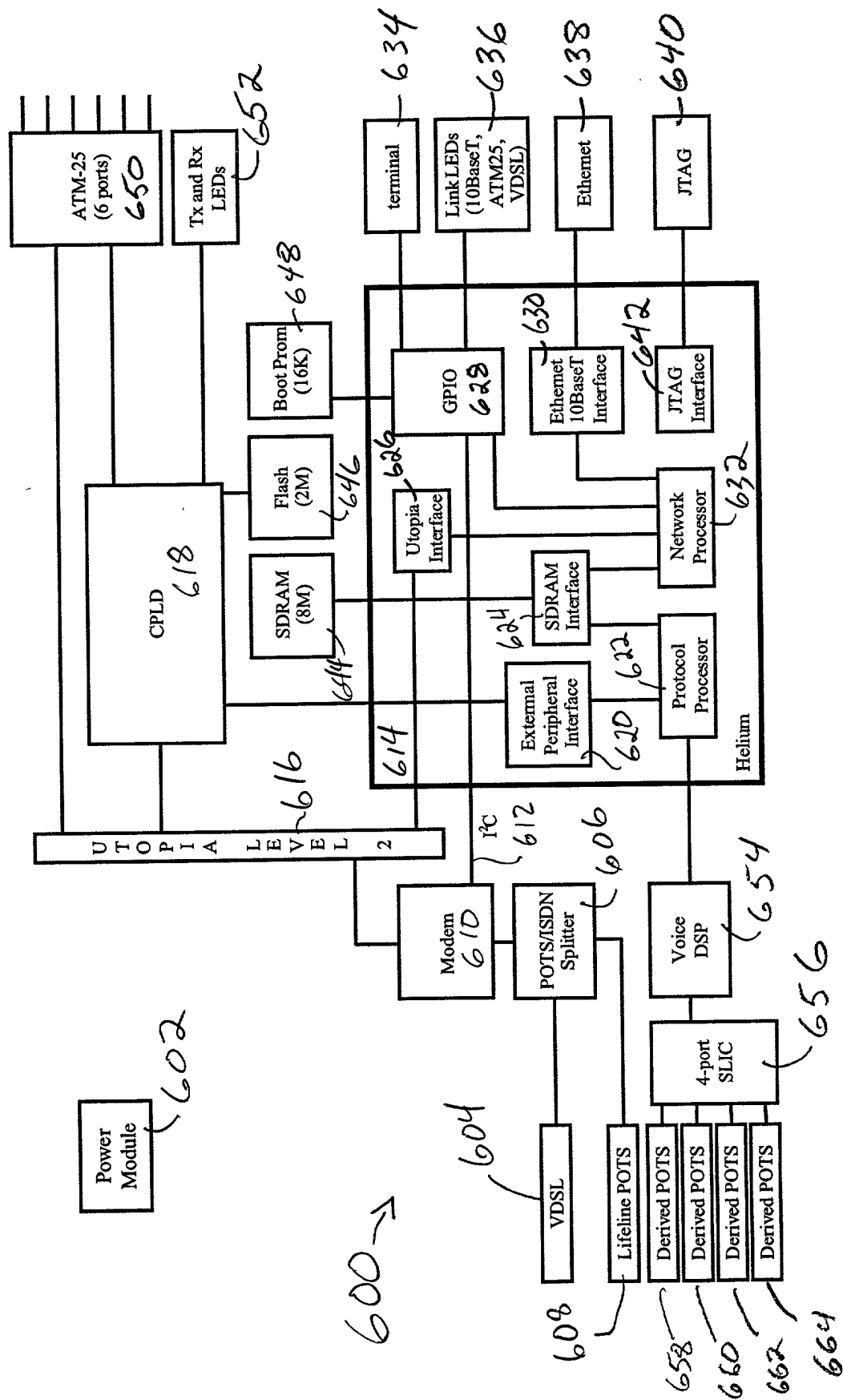
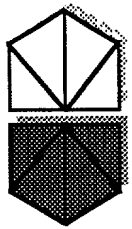


Fig. 7b



Typical Digital Set-Top Box

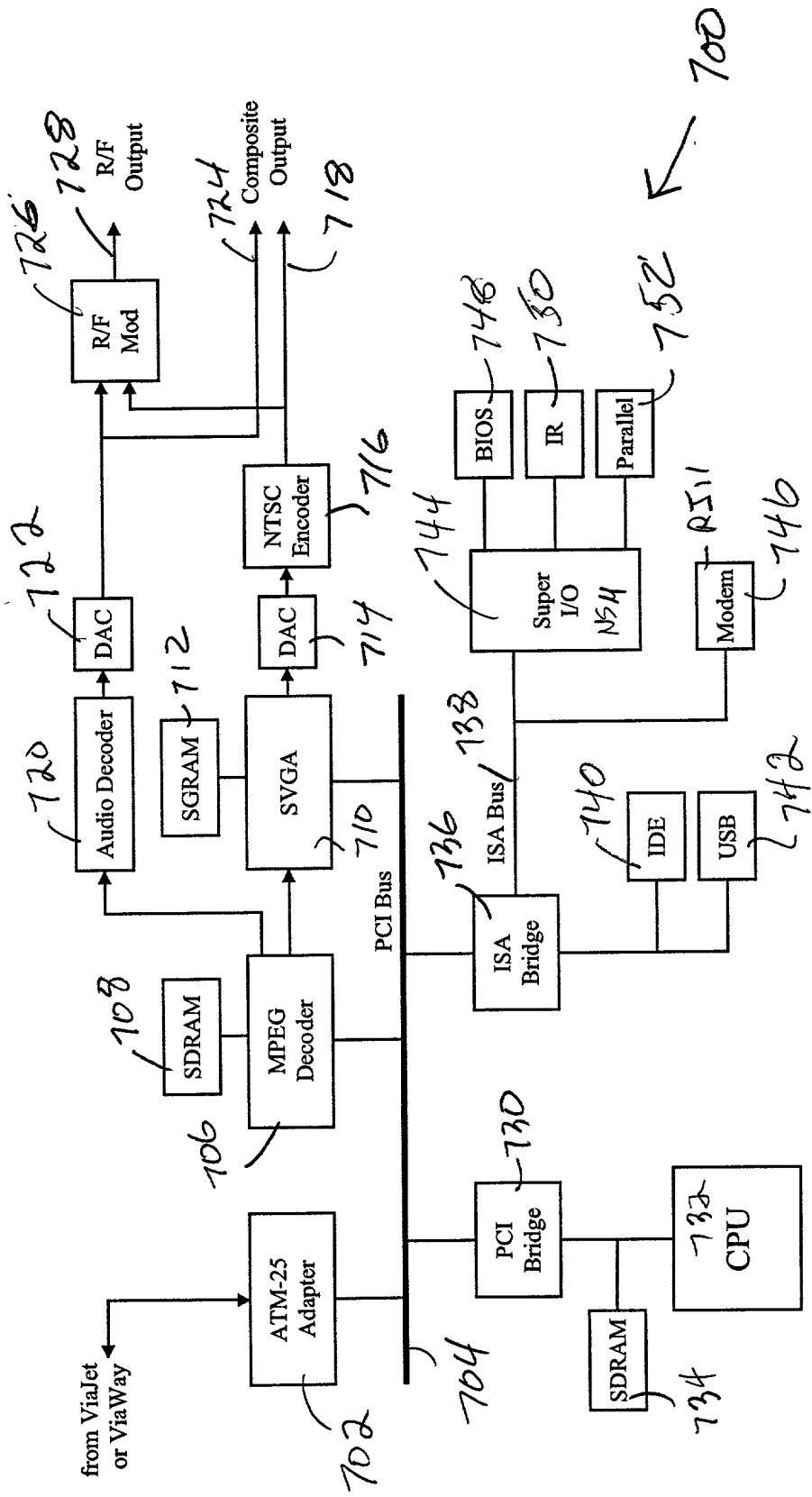


Fig. 700

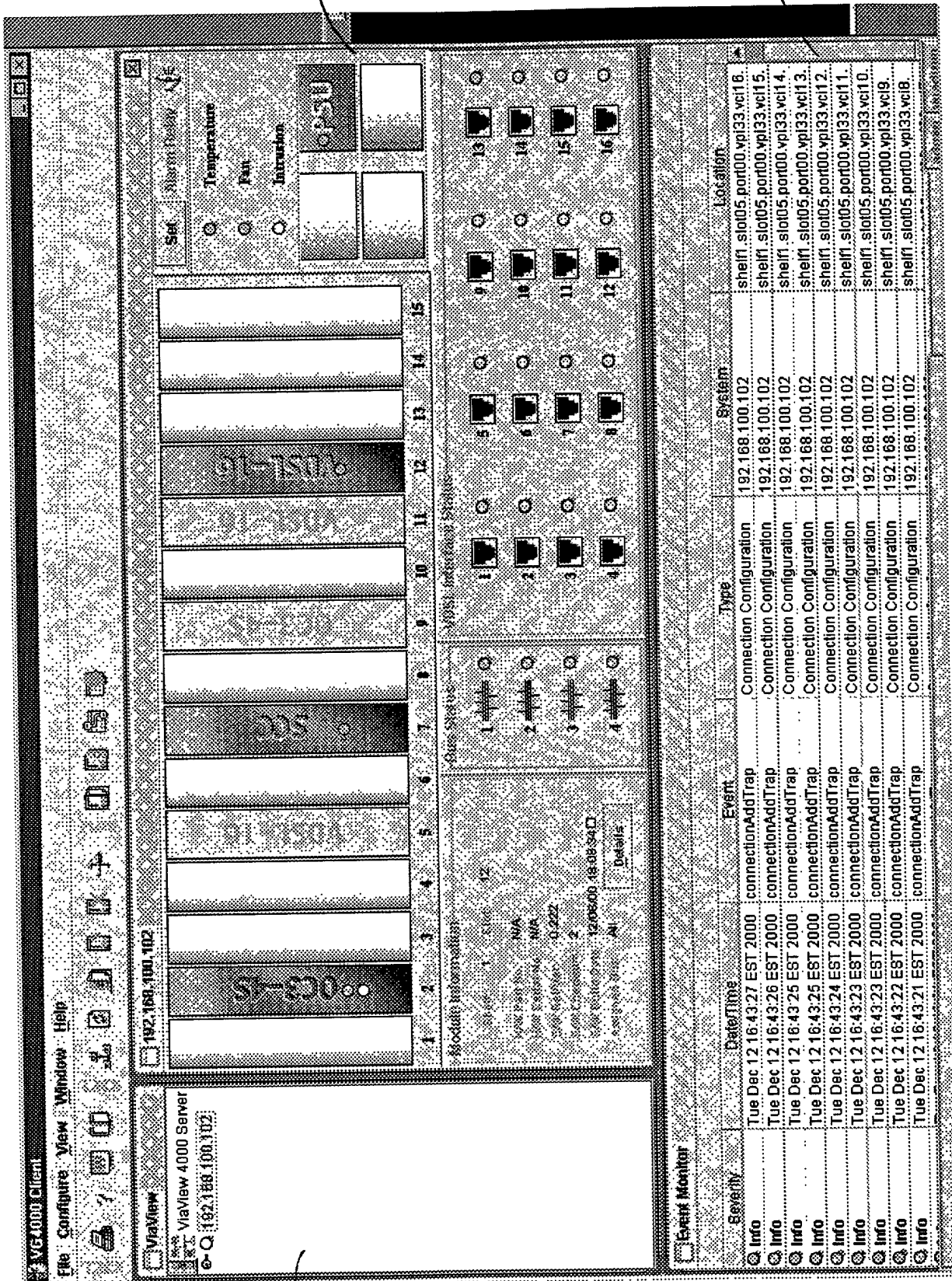
802

804

806

808

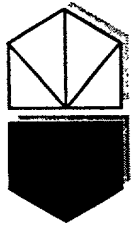
800



810

812

Fig. 8



Node Management Paths

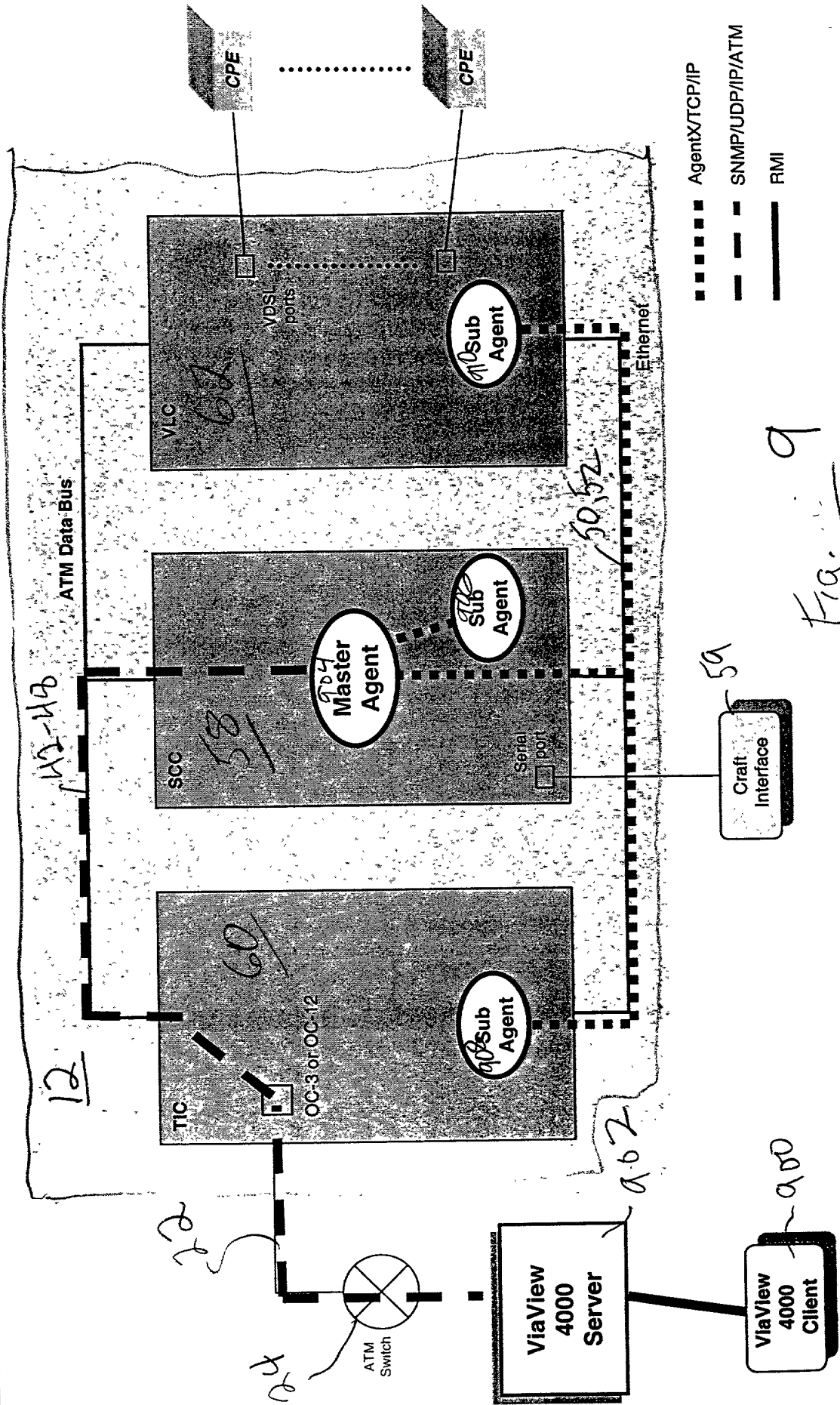
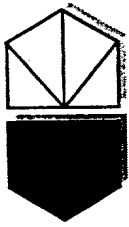


Fig. 9



CPE Management Path

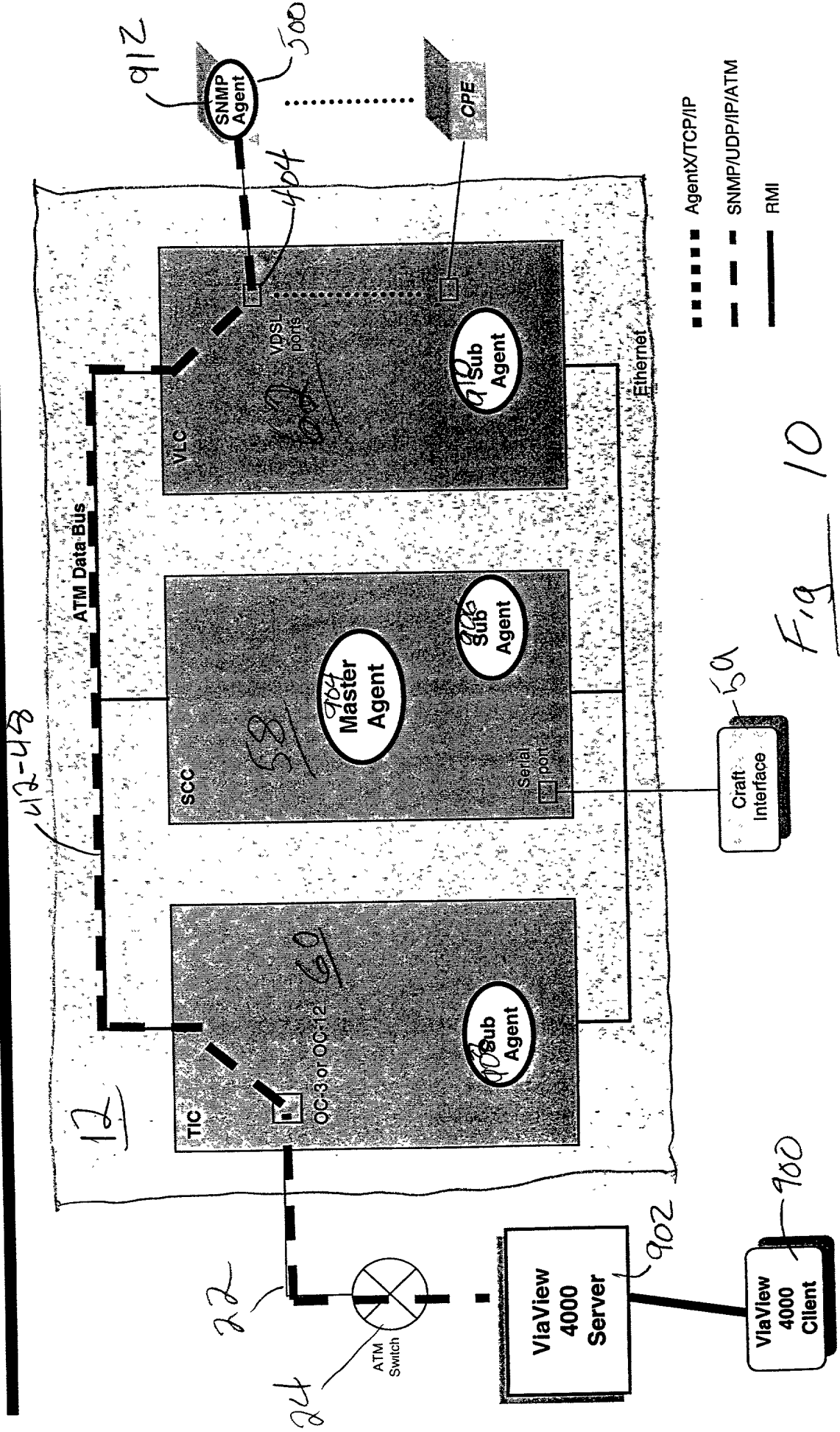
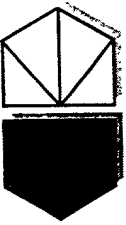


Fig 10



Connection Management

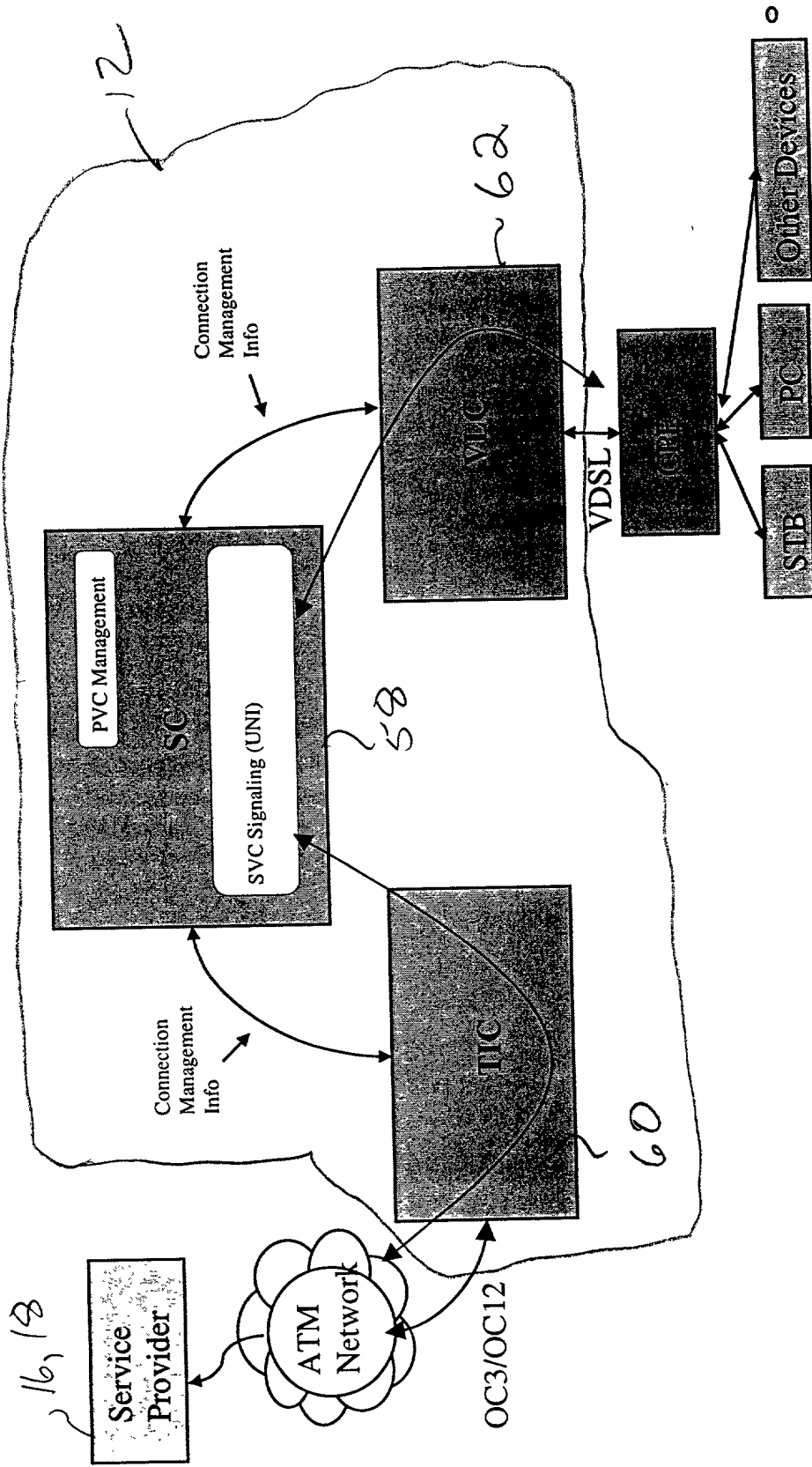
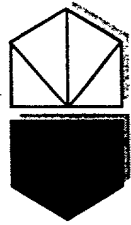


Fig. 11



CATV/Broadcast Management

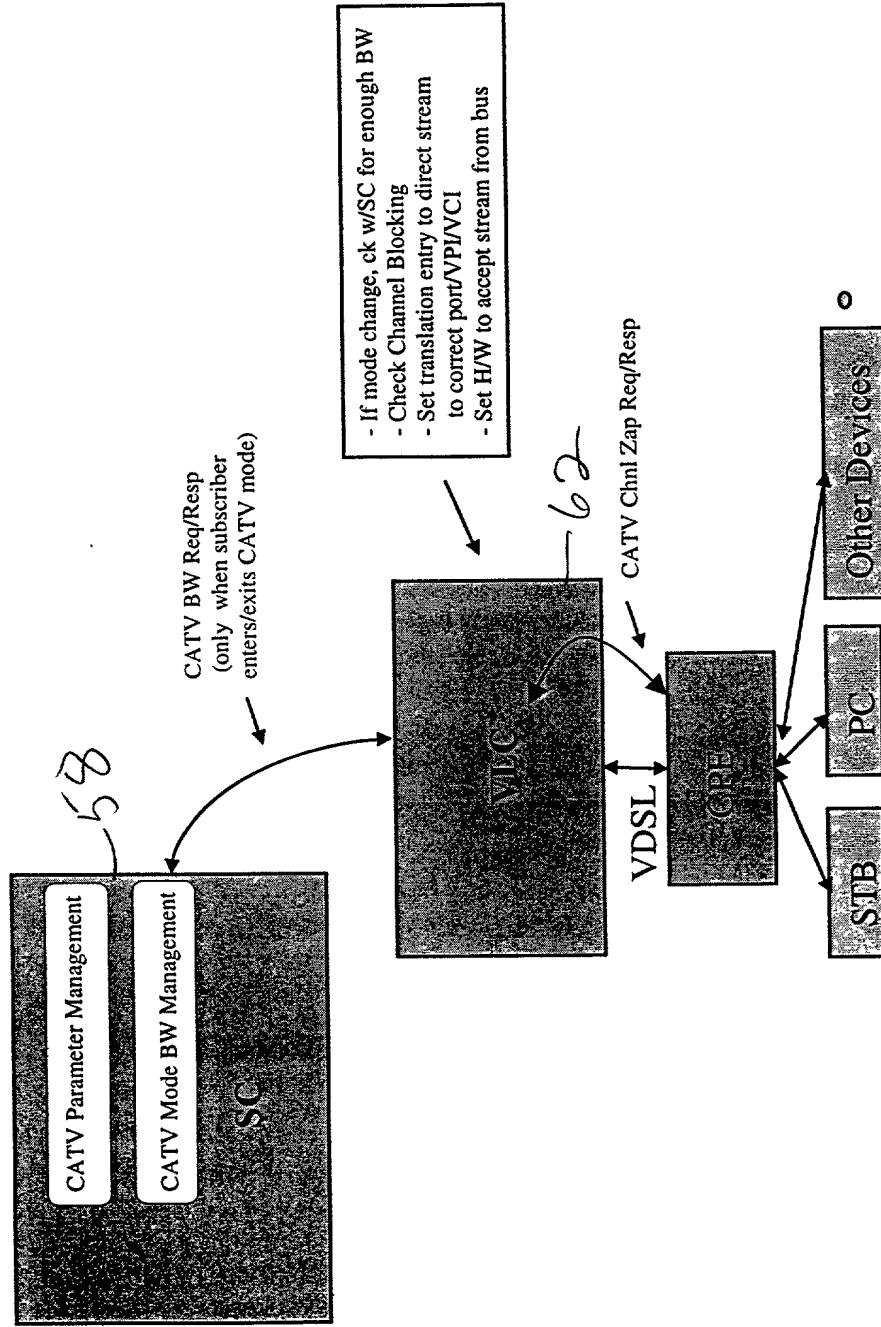


Fig. 12